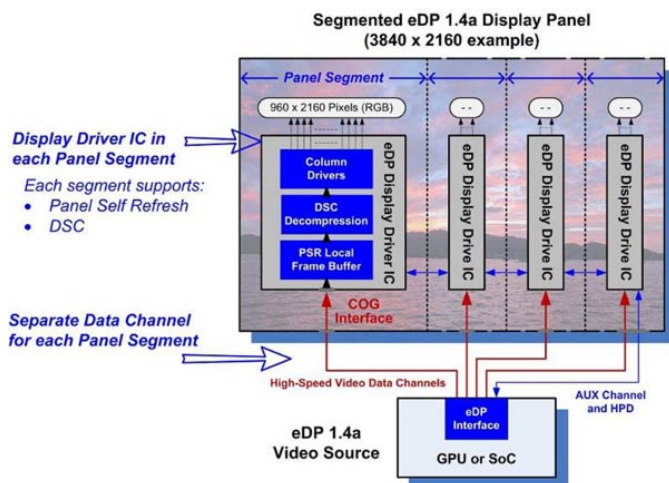


Embedded DisplayPort Reaches 1.4a

Written by Marco Attard
12 February 2015

The Video Electronics Standards Association (VESA) publishes the Embedded DisplayPort (eDP) standard version 1.4a-- a replacement to the February 2013 eDP v1.4 bringing an increase in video data transfer rate.



It incorporates the VESA Display Stream Compression (DSC) standard 1.1, as well as a segmented panel architecture allowing for higher panel integration.

Leveraging on [DisplayPort 1.3](#) as base specification, eDP 1.4a operates at the HBR3 link rate of 8.1Gbps per lane (for maximum bandwidth of 32.4Gbps), allowing it to support 8K resolution displays. Another addition is Multi-SST Operation (MSO), a technology supporting Segmented Panel Display, a VESA display architecture for thinner, lighter and lower-cost display panels.

MSO also allows the division of the 4 high-speed data lanes within the eDP interface into either 2 or 4 panel segments, allowing for higher level of integration on high-resolution displays.

The standard is already available to VESA members, and the association says displays with eDP 1.4a support should ship by 2016.

Go [VESA Publishes eDP Standard Version 1.4a](#)