

Cray Puts AMD Processors in CS500 Supercomputers

Written by Marco Attard
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Cray reunites with AMD as it announces the addition of Epyc processors in the CS500 cluster supercomputer line, all based on ultra-dense 2-way servers offering up to 64 cores, HDD/SSD and high-speed networking.



“Cray’s leadership in supercomputing is well known and AMD is thrilled to be working with them on the CS500 cluster system,” AMD says. “Cray is the first system vendor to offer an optimised programming environment for AMD EPYC processors, which is a distinct advantage. Combining AMD Epyc processors with Cray’s supercomputing expertise opens new opportunities for both companies to grow.”

The Cray CS-series supercomputers offer ultra-dense dual-socket nodes in a 2U chassis, with a system scaling up to 11000 nodes. The CS500 nodes based on AMD Epyc 7000-series processors are dual-socket nodes with two PCIe 2.0 gen 3 x16 slots, eight DDR4 memory channels/slots per socket and a choice of HDD/SSD storage. Four such machines fit into a 2U chassis, and the two PCIe slots per machine can plug into two 100GbE network cards to provide up to 200Gb/s network connectivity.

Also on offer is a 2U chassis with one node for large memory configurations (up to 4TB per box), visualisation and service node functionality complementing the compute node form factor. The system also include a version of the Cray software programming environment optimised for AMD Epyc processors.

The Epyc-based CS500 supercomputers will be available from Q3 2018.

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